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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/576,876	01/10/2007	Howard William Winter	011765-0350771	9924
909	7590	09/08/2008	EXAMINER	
PILLSBURY WINTHROP SHAW PITTMAN, LLP			VAUGHAN, MICHAEL R	
P.O. BOX 10500			ART UNIT	PAPER NUMBER
MCLEAN, VA 22102			2131	
MAIL DATE	DELIVERY MODE			
09/08/2008	PAPER			

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>
	10/576,876	WINTER, HOWARD WILLIAM
	<b>Examiner</b>	<b>Art Unit</b>
	MICHAEL R. VAUGHAN	2131

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

#### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

1) Responsive to communication(s) filed on 10 January 2007.  
 2a) This action is **FINAL**.                    2b) This action is non-final.  
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

4) Claim(s) 1-23 is/are pending in the application.  
 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.  
 5) Claim(s) \_\_\_\_\_ is/are allowed.  
 6) Claim(s) 1-23 is/are rejected.  
 7) Claim(s) 2-13 and 15-23 is/are objected to.  
 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

9) The specification is objected to by the Examiner.  
 10) The drawing(s) filed on 21 April 2006 is/are: a) accepted or b) objected to by the Examiner.  
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
 a) All    b) Some \* c) None of:  
 1. Certified copies of the priority documents have been received.  
 2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

1) Notice of References Cited (PTO-892)  
 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)  
 3) Information Disclosure Statement(s) (PTO/SB/08)  
 Paper No(s)/Mail Date 4/21/06.

4) Interview Summary (PTO-413)  
 Paper No(s)/Mail Date. \_\_\_\_\_.  
 5) Notice of Informal Patent Application  
 6) Other: \_\_\_\_\_.

## **DETAILED ACTION**

The instant application having Application No. 10/576876 filed on 4/21/06 is presented for examination by the examiner.

### ***Specification***

The following guidelines illustrate the preferred layout for the specification of a utility application. These guidelines are suggested for the applicant's use.

The disclosure is objected to because of the following informalities:

As provided in 37 CFR 1.77(b), the specification of a utility application should include the following sections in order. Each of the lettered items should appear in upper case, without underlining or bold type, as a section heading. If no text follows the section heading, the phrase "Not Applicable" should follow the section heading:

- (a) TITLE OF THE INVENTION.
- (b) CROSS-REFERENCE TO RELATED APPLICATIONS.
- (c) STATEMENT REGARDING FEDERALLY SPONSORED RESEARCH OR DEVELOPMENT.
- (d) THE NAMES OF THE PARTIES TO A JOINT RESEARCH AGREEMENT.
- (e) INCORPORATION-BY-REFERENCE OF MATERIAL SUBMITTED ON A COMPACT DISC.
- (f) BACKGROUND OF THE INVENTION.
  - (1) Field of the Invention.
  - (2) Description of Related Art including information disclosed under 37 CFR 1.97 and 1.98.
- (g) BRIEF SUMMARY OF THE INVENTION.
- (h) BRIEF DESCRIPTION OF THE SEVERAL VIEWS OF THE DRAWING(S).
- (i) DETAILED DESCRIPTION OF THE INVENTION.
- (j) CLAIM OR CLAIMS (commencing on a separate sheet).
- (k) ABSTRACT OF THE DISCLOSURE (commencing on a separate sheet).
- (l) SEQUENCE LISTING (See MPEP § 2424 and 37 CFR 1.821-1.825. A "Sequence Listing" is required on paper if the application discloses a nucleotide or amino acid sequence as defined in 37 CFR 1.821(a) and if the required "Sequence Listing" is not submitted as an electronic document on compact disc).

### ***Claim Objections***

Claims 2-13 and 15-23 are objected to because of the following informalities: each dependent claim, 2-13, recites "A" method in referring to the method its parent claim. This raises the question of whether or not it is the same method or a similar method of its parent claim. Using, "The" would solidify and clear up the relationship of the dependent claims. Similarly, claims 15-23 use "a" instead of "the".

### ***Claim Rejections - 35 USC § 112***

The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

Claim 15 rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the enablement requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to enable one skilled in the art to which it pertains, or with which it is most nearly connected, to make and/or use the invention. Claim 15 is a single means claim and does not appear in combination with the recited elements of claim 14. Furthermore data replication means covers all possible means for achieving writing and is not functionally connected to any recited structures. See MPEP 2164.08(a).

The following is a quotation of the second paragraph of 35 U.S.C. 112:  
The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claims 20-23 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

As per claim 20, it appears the Applicant has intended to write the claim in independent form but it is still a dependent claim of 14. As such, claim 20 should further limit the scope of the network analyzer card of claim 14. Similarly dependent claims 21-23 should likewise be written in dependent claim form congruent with claim 14.

Appropriate correction is required.

### ***Claim Rejections - 35 USC § 102***

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

Claims 1-20 are rejected under 35 U.S.C. 102(a) as being anticipated by the SCAMPI Prototype Implementation Report D2.2 published on Nov. 16<sup>th</sup>, 2003, hereinafter D22.

As per claim 1, D22 teaches receiving data from a network link (pgs. 10 and 17); replicating said data on board a network analyzer card (pg. 55) to produce at least two

editions of the received data (pgs. 18-19 and Fig. 2.6); and writing said editions of the received data to an area of memory in a host that is directly accessible by a host application (pgs. 19 and 25, particularly section 2.7.4).

As per claim 2, D22 teaches processing said editions of data stored in the said area of memory accessible by a host application, the processing comprising executing a different set of rules relating to intrusion detection on each edition (pg. 57).

As per claim 3, D22 teaches the data is replicated using hardware (pgs. 6 and 54).

As per claim 4, D22 teaches the editions of the received data are provided as independent data streams [unique packet buffers] (pg. 18).

As per claim 5, D22 teaches each of the at least two editions of said received data is buffered independently [has own buffer] (pg. 18).

As per claim 6, D22 teaches each of the independent data streams is filtered according to desired criteria (pg. 18, by classifiers and functions).

As per claim 7, D22 teaches different filtering rules are applied to each of the editions of the received data [each rule flow is created] (pg. 58).

As per claim 8, D22 teaches writing the editions of the received data to an area of kernel memory [kernel modules] of the host memory; (pg. 31 and 44) and providing to the host application an offset to enable location of the data by the host application in the kernel space of the memory (pg. 19, pointers).

As per claim 9, D22 teaches when data is written to the kernel space of the host memory a list of offsets with respect to a base address within kernel space is generated, the list of offsets serving to enable location of data packets within the kernel space with respect to the base address (pg. 19, pointer sets).

As per claim 10, D22 teaches providing to an application for running in application space, an offset (pointer) to enable location of the base address of the data within the kernel space (pg. 19).

As per claim 11, D22 teaches providing to the application a list of offsets with respect to the offset of the base address (pg. 19).

As per claim 12, D22 teaches the data is received as data frames from a network link (pg. 60).

As per claim 13, D22 teaches adding to substantially each of the received data frames a descriptor, the descriptor containing data relating to the data frame to which it is attached (pg. 19, capture length).

As per claim 14, D22 teaches a network analyzer card (pg. 55) for connection to a host and a network, the card comprising:

a receiver for receiving plural data frames from a network link (pg. 7);  
data replication means for generating at least two replica editions of the received data frames (pgs. 18-19 and Fig. 2.6); and  
a descriptor adder configured and arranged to add a descriptor [header] to substantially each of the data frames of each of the at least two replica editions of the received data

frames, the descriptor including data about the data frame [data length] to which it is attached for use in processing of the data frame (pg. 19).

As per claim 15, D22 teaches data writing means for writing the at least two replica editions of the received data frames to an area of host memory directly accessible by a host application (pg. 19, shared memory).

As per claim 16, D22 teaches the descriptor includes data indicative of the length of a data frame to which it is attached (pg. 19).

As per claim 17, D22 teaches the descriptor includes a timestamp indicative of the time at which the corresponding data frame was received at the network analyzer card (pg. 19).

As per claim 18, D22 teaches one or more of the data replication means, the descriptor adder and the data writing means is or are arranged in hardware (pg. 6).

As per claim 19, D22 teaches receiving data from a network link (pgs. 10 and 17);

replicating said data on board a network analyzer card (pg. 55) to produce at least two editions of the received data (pgs. 18-19 and Fig. 2.6); and writing said editions of the received data to an area of memory in a host that is directly accessible by a host application (pg. 19 and 25, section 2.7.4).

As per claim 20, D22 teaches a memory to receive at least two editions of the received data from the network analyzer card (pg. 19, shared memory); and at least two processors for processing said editions of the received data (pg. 7 and 27, plural processors including a dual processor)

***Claim Rejections - 35 USC § 103***

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 21-23 are rejected under 35 U.S.C. 103(a) as being unpatentable over D22 in view of USP 4,837,735, to Allen et al., hereinafter Allen.

As per claim 21, D22 teaches running this network device with more than one processor. On page 7, D22 mentions networks processors and even runs the device using a dual core processor on page 27. D22 also teaches that the sets of rules are run independently in their own flow according to a particular rule (pg. 58). While D22 teaches the use of more than one processor and running rules independently, there is no explicit teaching of assigning a set of rules to each processors but this is an obvious step in view of Allen. Allen teaches that each processor is responsible for running through a unique rule set (col. 15, lines 50-55). It is also known that dual processors are in fact designed for parallel processing to achieve greater throughput. Therefore it would have been obvious to one of ordinary skill in the art at the time of the invention to combine the teachings of D22 and Allen to use each processor to check data against sets of rules to improve greater throughput of the system.

As per claim 22, D22 teaches the rules relate to intrusion detection (pg. 57).

As per claim 23, D22 teaches the processors are arranged to execute rules of an intrusion detection system on data packets received by the host (pg. 57).

***Conclusion***

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

USP Application Publication 2004/0123141 teaches a multi-tier intrusion detection system.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to MICHAEL R. VAUGHAN whose telephone number is (571)270-7316. The examiner can normally be reached on Monday - Thursday, 7:30am - 5:00pm, EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ayaz Sheikh can be reached on 571-272-3795. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status

information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/M. R. V./

Examiner, Art Unit 2131

/Ayaz R. Sheikh/

Supervisory Patent Examiner, Art Unit 2131